

AUG 30 2005

TRANSMITTAL LETTER
(General - Patent Pending)

Docket No.
SEC.1111

In Re Application Of: Jong-Wook PARK et al.

Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/776,600	12 February 2004	YOHA, Connie C.	20987	2827	3485

Title: INTEGRATED CIRCUIT HAVING A NON-VOLATILE MEMORY CELL TRANSISTOR AS A FUSE DEVICE

COMMISSIONER FOR PATENTS:

Transmitted herewith is:

RESPONSE TO ELECTION/RESTRICTION REQUIREMENT

in the above identified application.

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Signature

Dated: 30 August 2005

KENNETH D. SPRINGER
Reg. No. 39,843
Volentine Francos & Whitt, P.L.L.C.
One Freedom Sqaure
11951 Freedom Drive, Suite 1260
Reston, VA 20190
Tel. No. 571-283-0720

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to the "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re PATENT APPLICATION of

Jong-Wook PARK et al.

Group Art Unit: 2827

Application No. 10/776,600

Examiner: YOHA, Connie C.

Filed: 12 February 2004

For: INTEGRATED CIRCUIT HAVING A NON-VOLATILE MEMORY CELL
TRANSISTOR AS A FUSE DEVICE

RESPONSE TO ELECTION/RESTRICTION REQUIREMENT

U.S. Patent and Trademark Office
Customer Service Window, Mail Stop Amendment
Randolph Building
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Sir:

In response to the Office Action of 1 August 2005, Applicants elect the invention of Group I, Claims 1-4, drawn to integrated circuit having non-volatile memory cells having floating gate and gate control adapted to store or program status information. The election is without traverse.

Respectfully submitted,

VOLENTINE FRANCOS & WHITT, PLLC

Kenneth D. Springer
Reg. No. 39,843

One Freedom Square
11951 Freedom Drive, Suite 1260
Reston VA 21090
Tel. (571) 283-0720
Fax (571) 283-0740

Date: 30 August 2005